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# Proving SAT does not have small circuits with an application to the two queries problem

Lance Fortnow<sup>a</sup>, A. Pavan<sup>b,\*</sup>, Samik Sengupta<sup>c</sup>

<sup>a</sup> *Department of Computer Science, University of Chicago, Chicago, IL 60637, USA*

<sup>b</sup> *Department of Computer Science, Iowa State University, Ames, IA 50011, USA*

<sup>c</sup> *Department of Computer Science and Engineering, University at Buffalo, Buffalo, NY 14260, USA*

Received 19 November 2003; received in revised form 19 July 2005

Available online 13 June 2007

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## Abstract

We show that if SAT does not have small circuits, then there must exist a small number of satisfiable formulas such that every small circuit fails to compute satisfiability correctly on at least one of these formulas. We use this result to show that if  $P^{NP[1]} = P^{NP[2]}$ , then the polynomial-time hierarchy collapses to  $S_2^P \subseteq \Sigma_2^P \cap \Pi_2^P$ . Even showing that the hierarchy collapsed to  $\Sigma_2^P$  remained open prior to this paper.

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*Keywords:* SAT; Small circuits; Two queries

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## 1. Introduction

Bshouty, Cleve, Gavaldà, Kannan and Tamon [2] give a probabilistic algorithm with a SAT oracle that learns circuits given hypothesis and membership queries to that circuit. If SAT has polynomial-size circuits, then one can use their algorithm to give a probabilistic procedure, once again with a SAT oracle, that finds that circuit. One can verify in co-NP that this circuit correctly computes SAT.

What if SAT does not have small circuits? Can one find a short witness of this fact? We give an affirmative answer. Building on Bshouty et al. we show that if SAT does not have polynomial-size circuits at length  $n$ , then for every  $k$  there are polynomial number of satisfiable formulas such that every circuit of size at most  $n^k$  fails to give a correct answer on at least one of these formulas.

In addition, one can find these formulas with a probabilistic algorithm with a SAT oracle. These satisfiable formulas along with satisfying assignments give a co-NP verifiable proof that SAT does not have  $n^k$ -size circuits.

We show an application to the following well studied question: Is one query to SAT as powerful as two queries to SAT? In the context of computing functions, Krentel [10] showed that if any function that can be computed by two

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\* Corresponding author.

*E-mail addresses:* [fortnow@cs.uchicago.edu](mailto:fortnow@cs.uchicago.edu) (L. Fortnow), [pavan@cs.iastate.edu](mailto:pavan@cs.iastate.edu) (A. Pavan), [samik@cse.buffalo.edu](mailto:samik@cse.buffalo.edu) (S. Sengupta).

<sup>1</sup> Part of the work done while the author was a postdoc at NEC Research Institute. Research supported in part by NSF grant CCF-0430807.

queries to SAT can be computed by one query, then  $P = NP$ , i.e., if  $P^{NP[1]} = P^{NP[2]}$ , then  $P = NP$ . It is natural to ask whether we can obtain such collapse if we focus on languages instead of functions.

Kadin [9] showed that if  $P^{NP[1]} = P^{NP[2]}$ , then the polynomial-time hierarchy collapses to  $\Sigma_3^P$ . Wagner [12] showed that the collapse can be improved to  $\Delta_3^P = P^{\Sigma_2^P}$ . Beigel, Chang, and Ogihara [1], building on the work of Wagner [12] and Chang and Kadin [6] obtained a stronger conclusion. They showed that every language in the polynomial-time hierarchy can be solved by a polynomial-time machine that makes at most one NP query and one  $\Sigma_2^P$  query.

Buhrman and Fortnow [3] showed many other collapses including that polynomial-time hierarchy collapses to  $BPP^{NP}$ . They tried to improve their collapse to  $\Sigma_2^P$  but they could not find a way to easily determine whether SAT had small circuits.

Using our lemma we can solve this problem and achieve the collapse. We show that if  $P^{NP[1]} = P^{NP[2]}$  then PH collapses to  $S_2^P \subseteq ZPP^{NP}([4]) \subseteq \Sigma_2^P \cap \Pi_2^P$ .

## 2. Preliminaries

Given  $k > 0$ ,  $P^{NP[k]}$  denotes the class of languages accepted by a polynomial-time-bounded oracle Turing machine that makes at most  $k$  adaptive queries to SAT.

The class  $S_2^P$  has been defined independently by Russell and Sundaram [11] and Canetti [5]. A set  $L$  is in  $S_2^P$  if there is a polynomial-time predicate  $R$  and a polynomial  $p(\cdot)$  such that

$$\begin{aligned} x \in L &\Rightarrow \exists y \forall z R(x, y, z), \quad \text{and} \\ x \notin L &\Rightarrow \exists z \forall y \neg R(x, y, z), \end{aligned}$$

where  $|y|, |z| \leq p(|x|)$ .

The class  $S_2^P$  can be viewed as a game among two competing provers and a polynomial-time verifier. The first prover is trying to convince the verifier that the string is in the language, and the second prover is trying to convince the verifier that the string is not in the language. If the input  $x$  belongs to  $L$ , then the first prover can give an irrefutable proof  $y$  of this fact, i.e., the verifier will accept irrespective of the proof given by the second prover. Similarly, if the string does not belong to the language, then the second prover can furnish an irrefutable proof.

## 3. Key lemma

In this section we show that if SAT does not have polynomial-size circuits, then for every  $k$  there exist polynomially many formulas such that every circuit of size  $n^k$  is wrong on at least one of these formulas.

Throughout this paper, we assume without loss of generality that if a circuit says that a formula is satisfiable, then it outputs a satisfying assignment. Thus the circuit can make errors on only one side. This implies that the language  $\{ \langle C, 1^n \rangle \mid C \text{ is wrong on a formula of size } n \}$  is in NP.

**Lemma 3.1.** *Fix  $n > 0$ . For every  $k > 0$ , if SAT does not have  $n^{k+2}$ -size circuits at length  $n$ , then there exists a set  $S$  of satisfiable formulas of length  $n$ , called counter-examples, such that every circuit of size  $n^k$  is wrong on at least one formula from  $S$ . The cardinality of  $S$  is polynomial in  $n$ .*

**Proof.** The proof uses ideas from Bshouty et al. [2]. We define a probabilistic process and show that if SAT does not have  $n^{k+2}$ -size circuits, then the probabilistic process outputs a set of counter-examples with nonzero probability. We build the set  $S$  of counter-examples in stages. At stage zero,  $S$  contains an arbitrary satisfiable formula. At each stage we add a formula to the set. Therefore, after  $i - 1$  stages,  $S$  has  $i$  counter-examples. We now describe stage  $i$ . Fix  $m = 36n$ .

Let  $T_i$  be the set of all  $n^k$ -size circuits that are correct on  $S$ . If  $T_i$  is empty, then we are done; so assume  $T_i$  is not empty. Uniformly and independently pick  $m$  circuits  $c_1, c_2, \dots, c_m$  from  $T_i$ . Let  $C$  be a circuit that takes majority vote of  $c_1, \dots, c_m$ . Note that the size of  $C$  is at most  $n^{k+2}$ . Since SAT does not have  $n^{k+2}$ -size circuits, there exists a satisfiable formula  $\phi$  on which  $C$  is not correct. Add  $\phi$  to  $S$ . This completes stage  $i$ .

We claim that after polynomially many stages,  $T_i$  is empty. Thus  $S$  contains polynomially many formulas such that every circuit of size  $n^k$  is wrong on at least one formula in  $S$ .

**Claim 3.2.**

$$Pr[\|T_{i+1}\| \leq 2/3\|T_i\|] > 0.$$

**Proof.** Denote the set of randomly chosen circuits by  $U$ . Given a formula  $\rho$ , let  $V_\rho$  be the set of all circuits in  $T_i$  that are correct on  $\rho$ . Call a formula  $\rho$  “bad” if  $\|V_\rho\| > 2/3\|T_i\|$ . In the following, we fix a bad  $\rho$ .

For  $1 \leq i \leq m$ , define random variables  $X_i$  as follows:  $X_i = 1 \Leftrightarrow c_i \notin V_\rho$ . Since  $c_i$ 's are picked independently and uniformly,  $Pr[X_i = 1] = p \leq \frac{1}{3}$  for every  $i$ ,  $1 \leq i \leq m$ . We note that since  $p \leq \frac{1}{3}$ ,

$$Pr\left[\|U \cap V_\rho\| \leq \frac{1}{2}\|U\|\right] \leq Pr\left[\frac{\sum_{i=1}^m X_i}{m} - p > \frac{1}{6}\right].$$

Applying the Chernoff bound [7, page 11] on the right-hand side, we can show that

$$Pr\left[\|U \cap V_\rho\| \leq \frac{1}{2}\|U\|\right] \leq 2e^{-m*(1/18)} < 1/2^{2n}.$$

Since there can be at most  $2^n$  bad formulas,

$$Pr\left[\exists \text{ bad } \rho \text{ such that } \|U \cap V_\rho\| \leq \frac{1}{2}\|U\|\right] < 1/2^n. \tag{1}$$

Consider the counter-example  $\phi$  generated during stage  $i$ . Since  $\phi$  is a counter-example to  $C$ , the majority circuit of  $c_1, \dots, c_m$ , more than  $m/2$  circuits in  $U$  are wrong on  $\phi$ . However, if this  $\phi$  were a bad formula, then by Eq. (1), with high probability, more than half the circuits from  $U = \{c_1, \dots, c_m\}$  would be correct on  $\phi$ . It follows that the probability that  $\phi$  is not bad is nonzero. Thus  $\|V_\phi\| \leq 2/3\|T_i\|$  with high probability. Note that every circuit in  $T_{i+1}$  should be correct on  $\phi$ . Thus it follows that  $\|T_{i+1}\| \leq 2/3\|T_i\|$  with nonzero probability. This proves Claim 3.2.  $\square$

Therefore, after each stage, with nonzero probability, the number of circuits that are correct on  $S$  are reduced by a constant fraction. So after polynomially many stages all the  $n^k$ -size circuits would be wrong on  $S$ . Since we increase the size of  $S$  by one during each stage, the cardinality of  $S$  is bounded by a polynomial.  $\square$

We also note that the above process can be implemented by a probabilistic polynomial-time-bounded machine that uses SAT as an oracle. At any stage we need the ability to pick circuits  $c_1, c_2, \dots, c_m$  uniformly at random from  $T_i$ , and generate a counter-example  $\phi$  to  $C$  where  $C$  is the circuit that takes majority vote of  $c_1, \dots, c_m$ . The later task can be done by making queries to the following NP language.

$$\{(C, x) \mid \exists \text{ a satisfiable formula } \phi \text{ such that } x \text{ is a prefix of } \phi \text{ and } C \text{ is wrong on } \phi\}.$$

Also note that once we obtain the counter-example  $\phi$ , we can compute a satisfying assignment of  $\phi$  using SAT as an oracle. So we can assume that  $S$  consists of satisfiable formulas along with the assignments. Now

$$T_i = \{C \mid C \text{ is a } n^k\text{-size circuit that is correct on } S\}.$$

Since  $S$  consists of satisfiable formulas along with the assignments,  $T_i$  is a set in P. Jerrum, Valiant, and Vazirani [8] showed that picking elements, in an approximately uniform manner, from a set in P can be done in polynomial-time using SAT as an oracle. Using their procedure we can pick circuits from  $T_i$  in an approximately uniform manner.

**4. Application to two queries**

In this section we show an application of our lemma to the two queries problem.

**Theorem 4.1.** *If  $P^{NP[1]} = P^{NP[2]}$ , then  $PH = S_2^P$ .*

To prove Theorem 4.1 we need the following theorem by Buhrman and Fortnow [3].

**Theorem 4.2 (Buhrman–Fortnow).** *If  $P^{NP[1]} = P^{NP[2]}$ , then there exists a polynomial-time predicate  $R$  and a constant  $k > 0$  such that for every  $n$ , one of the following holds.*

- (1) *Locally NP = co-NP: For every unsatisfiable formula  $\phi$  of length  $n$ , there is a short proof of unsatisfiability  $w$ , i.e.,  $\phi \notin \text{SAT} \Leftrightarrow \exists w R(\phi, w)$ , where  $|w|$  is bounded by a fixed polynomial in  $n$ .*
- (2) *There exists a circuit of size  $n^k$  that decides SAT at length  $n$ .*

We first show that if  $\text{P}^{\text{NP}[1]} = \text{P}^{\text{NP}[2]}$ , then  $\Sigma_2^P = \Pi_2^P$ . We use Lemma 3.1 to decide whether locally NP = co-NP or SAT has small circuits.

**Lemma 4.3.** *If  $\text{P}^{\text{NP}[1]} = \text{P}^{\text{NP}[2]}$ , then  $\Sigma_2^P = \Pi_2^P$ .*

**Proof.** Let  $L$  be any language in  $\Pi_2^P$ . For any input  $x$ , the following holds:

$$x \in L \Leftrightarrow \forall y \phi_y \in \text{SAT}.$$

Let  $|\phi_y| = m$ . By Theorem 4.2, if SAT does not have  $m^{k+2}$ -size circuits at length  $m$ , then every unsatisfiable formula of length  $m$  has a short proof of unsatisfiability.

We describe an NP machine with SAT as an oracle that accepts  $L$ . Recall that the set  $\{\langle C, 1^n \rangle \mid C \text{ is wrong on a formula of length } n\}$  is in NP.

Consider the following machine  $M$ :

- (1) Guess 0 or 1.
- (2) If the guessed bit is 0, guess a circuit  $C$  of size  $m^{k+2}$ , and ask the SAT oracle if  $C$  is a correct circuit for SAT at length  $m$ . If the answer is “no,” then reject the input. If the answer is “yes,” then  $C$  is a correct circuit for SAT at length  $m$ . This can be used to decide  $x$ , by asking the SAT oracle whether there is a  $y$  such that  $C(\phi_y) = 0$ . If the answer is “yes,” then  $x$  does not belong to  $L$ ; otherwise,  $x$  belongs to  $L$ .
- (3) If the guessed bit is 1, guess  $l$  satisfiable formulas  $\phi_1, \dots, \phi_l$  and ask the SAT oracle whether there is a circuit of size at most  $m^k$  that is correct on all the guessed formulas. (Note that  $l$  is the number of counter-examples obtained from Lemma 3.1.) If the answer is “yes,” then reject the input. If the answer is “no,” then there is no circuit (for SAT) of size  $m^k$  at length  $m$ . In this case, by Theorem 4.2, there is a polynomial-time predicate  $R$  such that for every unsatisfiable formula of length  $m$ , there is a short proof  $w$ . Ask the SAT oracle if  $x$  is in the following set:

$$\{x \mid \exists y \exists w R(\phi_y, w)\}.$$

If  $x$  is in this set, then reject  $x$ , otherwise accept  $x$ .

We claim that the above algorithm is correct. Let  $x \in L$ . We consider the following two cases.

**Case 1.** SAT has  $m^{k+2}$ -size circuits at length  $m$ . In this case there exists a path of  $M$  that guesses the correct circuit and the machine accepts along this path.

**Case 2.** SAT does not have  $m^{k+2}$ -size circuits at length  $m$ . In this case, by Lemma 3.1, there exists a set of satisfiable formulas  $\phi_1 \dots \phi_l$  such that every circuit of size  $m^k$  is wrong on at least one of the formulas. Therefore, there is a path of  $M$  that correctly guesses these  $\phi_1, \dots, \phi_l$ . Along this path  $M$  knows that NP = co-NP locally. So  $M$  accepts  $x$  along this path.

Next we show that if  $x$  does not belong to  $L$ , then every path of the machine rejects  $x$ . Again we treat two cases.

**Case 1.** SAT has  $m^{k+2}$ -size circuits at length  $m$ . Consider the paths that guessed 0 in the first step. The path that correctly guesses the circuit rejects. The paths that guess a wrong circuit also reject. Now, consider that paths the guessed 1. In this case, there may or may not exist a set of counter-examples against  $m^k$ -size circuits. If there are no counter-examples, then all paths reject. If there are counter-examples, then some paths will guess the correct counter-examples. However, the existence of counter-examples to  $m^k$ -size circuits implies that SAT does not have  $m^k$ -size circuits at length  $m$ . Thus by Theorem 4.2, locally NP = co-NP. Thus all these paths correctly decide that  $x \notin L$ .

**Case 2.** SAT does not have  $m^{k+2}$ -size circuits at length  $m$ . In this case all the paths that guessed 0 in the first step reject. Consider the paths that guessed 1. By Lemma 3.1, there exists a set of counter-examples. The path that correctly guesses the counter-examples realizes that locally  $\text{NP} = \text{co-NP}$ , and rejects  $x$ . The paths that guess wrong counter-examples also reject.

Therefore,  $M$  decides  $L$ . This shows that  $\Sigma_2^P = \Pi_2^P$ .  $\square$

Theorem 4.1 follows from Lemma 4.3 and the following lemma.

**Lemma 4.4.** *If  $\text{P}^{\text{NP}[1]} = \text{P}^{\text{NP}[2]}$ , then  $\Sigma_2^P \cap \Pi_2^P = \text{S}_2^P$ .*

**Proof.** Let  $L$  be in  $\Sigma_2^P \cap \Pi_2^P$ . Thus

$$x \in L \quad \Rightarrow \quad \exists y \phi_y \notin \text{SAT} \wedge \forall z \rho_z \in \text{SAT},$$

$$x \notin L \quad \Rightarrow \quad \exists z \rho_z \notin \text{SAT} \wedge \forall y \phi_y \in \text{SAT}.$$

Without loss of generality, assume that  $|\phi_y| = |\rho_z| = m$ . By Theorem 4.2, at length  $m$  either every unsatisfiable formula has a short proof of unsatisfiability, or there is a  $m^k$ -size circuit that decides SAT at length  $m$ .

In the former case, i.e., if every unsatisfiable formula has a short proof of satisfiability, the first prover's proof consists of  $y$ ,  $\phi_y$ , and a proof that  $\phi_y$  is not satisfiable. And the second prover's proof consists of  $z$ ,  $\rho_z$ , and a proof that  $\rho_z$  is not satisfiable.

In the later case, the first prover's proof consists of  $y$ ,  $\phi_y$ , and a circuit of size  $m^k$ . The second prover's proof consists of  $z$ ,  $\rho_z$ , and a circuit of size  $m^k$ .

Upon receiving the proofs, the verifier executes the following algorithm. If either prover claims a short proof of unsatisfiability, then the verifier first checks whether the given short proof really proves that the formula in consideration ( $\phi_y$  or  $\rho_z$ ) to be unsatisfiable. The verifier accepts if the first prover's proof is correct and rejects if the second prover's proof is correct. Note that both of them cannot be correct.

Consider the case where both the provers give circuits. Here, the first prover is claiming that  $\phi_y$  is unsatisfiable, and the second prover is claiming that  $\rho_z$  is unsatisfiable. Also, the first prover is implicitly claiming that for every  $z$ ,  $\rho_z$  is satisfiable. Therefore, if the first prover is correct, then his circuit should be able to output a satisfying assignment of  $\rho_z$  given by the second prover. The verifier checks whether that is the case. The verifier accepts only if the first prover's circuit produces a satisfying assignment on  $\rho_z$ .

It is clear that the prover who gives a correct proof can convince the verifier. Therefore,  $L$  is in  $\text{S}_2^P$ .  $\square$

## 5. Further work

It would be interesting to see whether more applications of Lemma 3.1 can be found. Can we improve the collapse in Theorem 4.1 to  $\text{P}^{\text{NP}}$ ? What consequences can be obtained if one assumes  $\text{P}^{\text{NP}[k]} = \text{P}^{\text{NP}[k+1]}$  for  $k \geq 2$ ?

## Acknowledgments

The authors thank Richard Chang for his comments on an earlier version of this paper. The third author thanks Alan Selman for his helpful insights and valuable suggestions.

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